

Sept. 29, 2021 AKI FUJIMURA, CEO, D2S, Inc.

A general formula for deep learning success in semiconductor manufacturing

Ajay Baranwal, Aki Fujimura

Many Industries Have Had Deep Learning Success



















So Why Does Semiconductor Manufacturing Seem Stalled?



There Have Been Many Papers





CENTER
FOR DEEP LEARNING
IN ELECTRONICS
MANUFACTURING





















TASMIT, Inc.

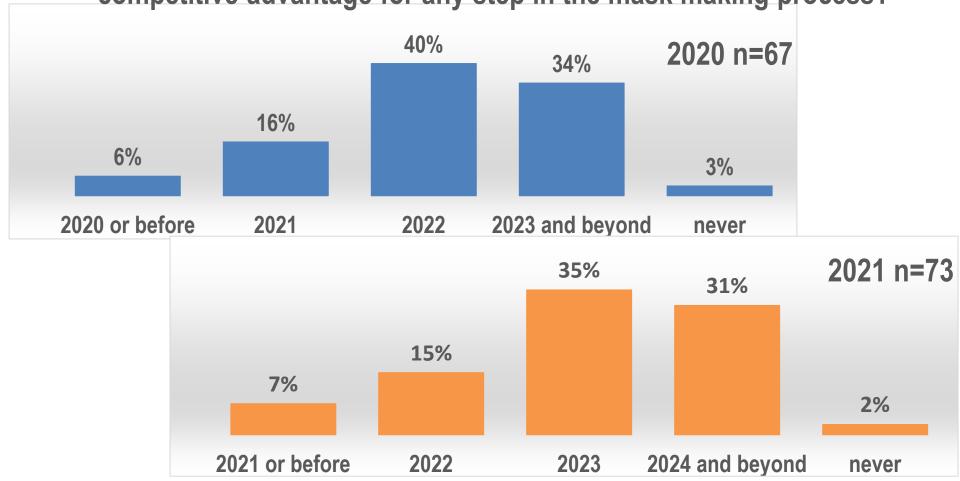
And yet...



Deep Learning Predictions Shift to 2023 & Beyond Only 22% say 2022 vs 62% in last year's survey



In the mask industry, when will capabilities based on deep learning become a competitive advantage for any step in the mask making process?

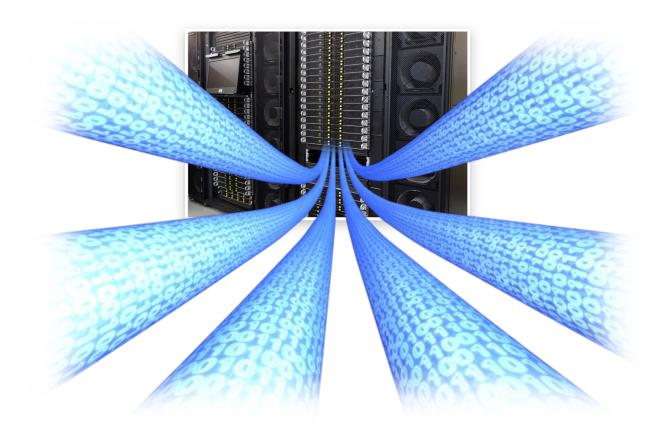


Prototype: Easy; Production: Hard





Doesn't DL Need Data? We Can't Get Data...



- DL is only as accurate as it's trained to be = masses of data
- Data belongs to the customer
- Mask shops are good at not making too many samples of anomalous data

Simulation- and DL-based digital twins are essential!



Can a Statistical Method Work for Mask Making?





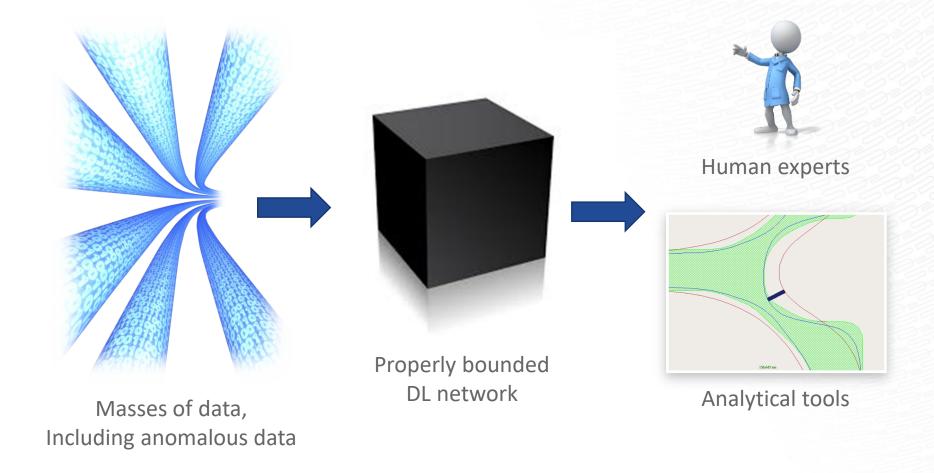


Quality Required > 7σ

The key is to apply DL carefully!



Can We Trust the Results?



Yes, with properly bounded networks and output checks



Essential Ingredients for DL Success



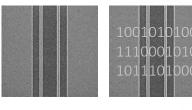
Executive sponsor with *long-term* commitment to DL

DL Experts



Immersion of talent





Actual SEM Digital Twin

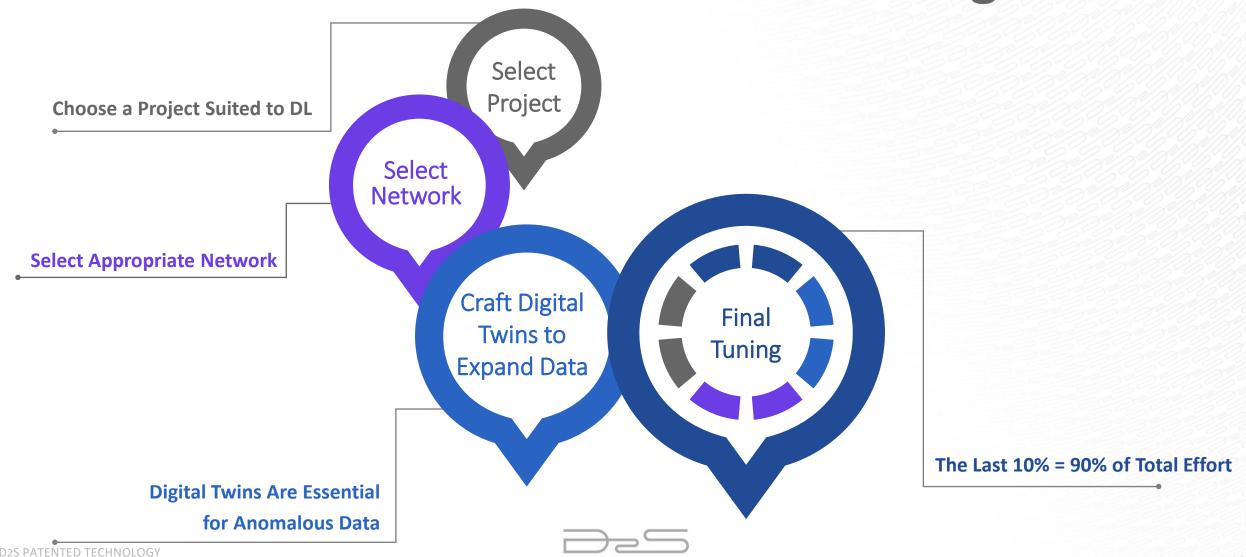
Lots of data



PFLOPS of Computing



General Formula for DL Success in Semiconductor Manufacturing



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Selecting the Right Projects

Not Suited to DL

- DL-only constructive tools (ILT, MPC)

 DL is statistical: makes mistakes,

 though can be bounded
- eBeam or litho simulation

 Faster and more accurate analytically
- DL-only verification tools (MRC)

 DL is statistical: makes mistakes,
 though can be bounded

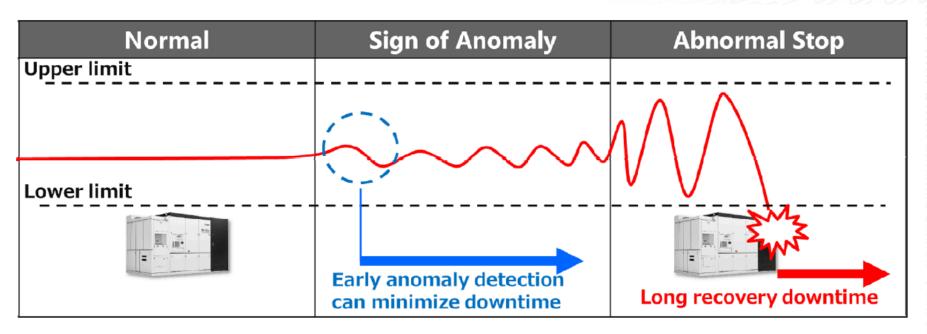
Good for DL

- Initial-condition acceleration for iterative optimization
- Estimations for other tools
- ✓ Prediction applications
- Categorization



Good Choice for DL Canon: Service Interval Prediction

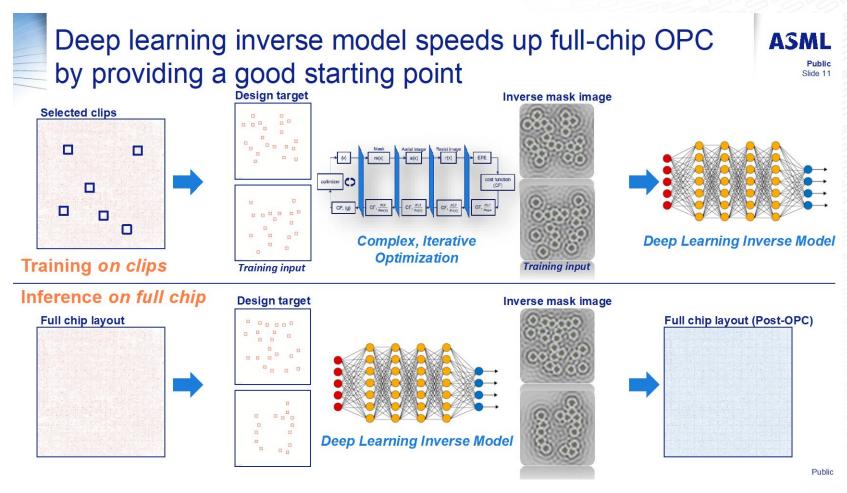
Canon



YOSUKE TAKARAGA, et al., LITNOGRAPHY TOOI IMPROVEMENT AT PROGUCTIVITY AND PERFORMANCE WITH GATA ANALYSIS AND MACHINE learning," SPIE Advanced Lithography, 2021



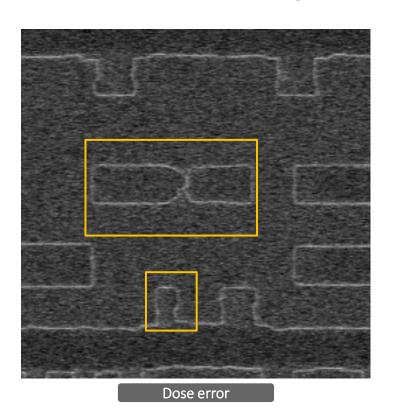
Good Choice for DL ASML: Acceleration of OPC

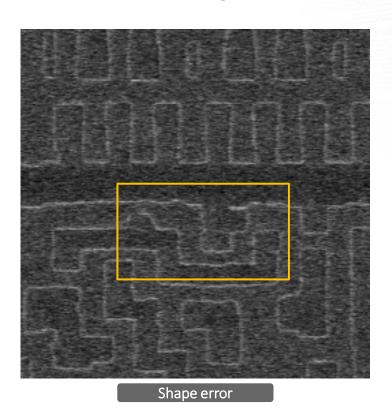


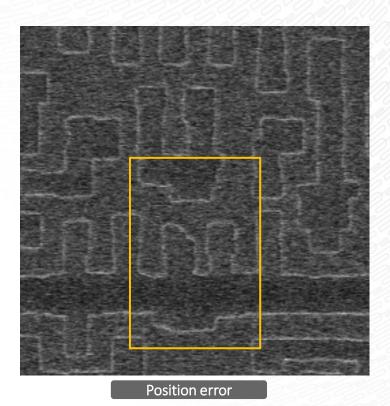


Good Choice for DL NuFlare's VSB Writer Failure Diagnosis

800K data points trained a 26-layer, 12 million-node network





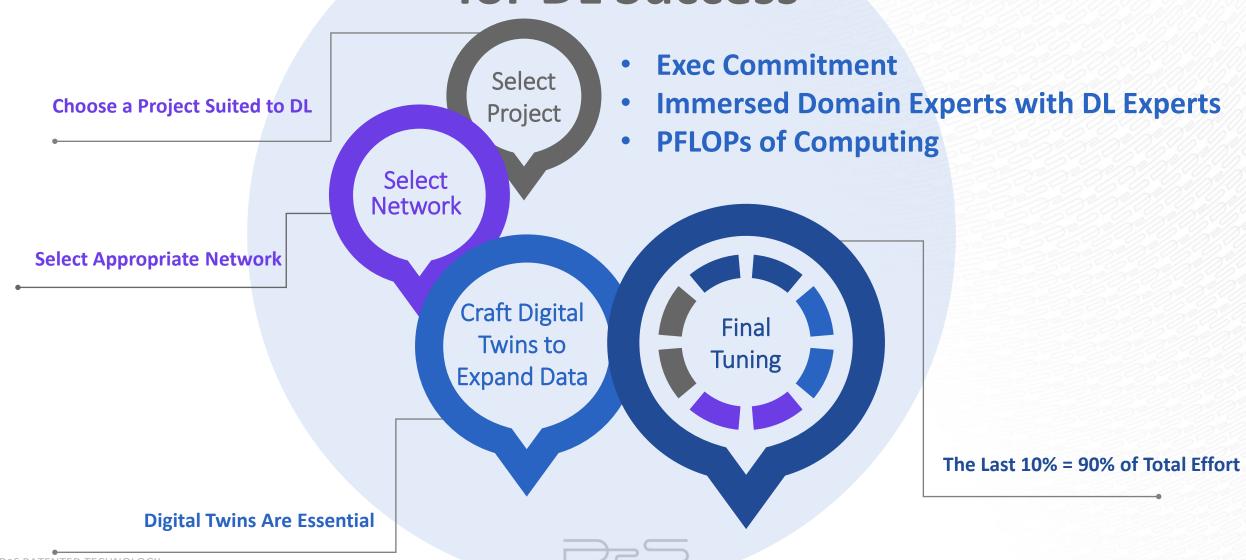


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Commitment and Digital Twins Essential for DL Success



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